For our location in **Gratkorn** we are looking for a
Junior Physical Design Engineer (m/f/d)

With segments dedicated to smart cities, retail, payments, identification and IoT Security, the Secure Edge Identification (SEI) team provides a broad set of solutions to protect what's most valuable for people – be it data, goods, or private information. The products within the group enable large diversity of applications which are set for long-term success, leveraging well-established brands in our portfolio like MIFARE, UCODE, NTAG, EdgeLock SE and more!

Your responsibilities:
- Digital block and top-level layout
- Clock tree definition and implementation
- Floorplanning incl. power routing
- Static timing analysis
- Low power implementation incl. CPF/UPF
- Support of digital design, failure analysis and security certification teams in physical design topics

Your profile:
- Master's/Bachelor's Degree in Electrical/Electronic engineering, Information and Computer Engineering, Telecommunications Engineering, Biomedical Engineering or comparable education
- Good knowledge in digital design
- Programming experience, preferably TCL
- Knowledge of Cadence Encounter/Innovus tool
- Basic know-how on standard Place&Route flow is beneficial
- Know-how on SDC constraints and STA is beneficial
- Good physical understanding of CMOS processes is beneficial
- Good communication skills in English
- Structured way of working
- Open to new ideas and a creative attitude taking initiative to spot and implement improvements
- Willing to take responsibility for secure element and NFC products and drive leading edge solutions
- Engaged, self-motivated, flexible and enthusiastic team player

Ready to create a smarter world? Apply online via the QR code or check it out for further info!
If you have further questions, feel free to send an email to lisa.ziermann@nxp.com.

At NXP in Austria, we are proud to have received the Leading Employer Award 2020, 2021, and 2022 which is presented exclusively only to the **top 1% of employers in Austria**.

NXP Talent Acquisition Team EMEA